

	Search Terms
1	BOARD
2	BOARDS
3	FUNCTIONAL
4	FUNCTIONALS
5	LATCH
6	LATCHED
7	LATCHES
8	LATCHES
9	LATCHING
10	LATCHINGS
11	PATH
12	PATHS
13	REQUEST
14	REQUESTED
15	REQUESTEDS
16	REQUESTING
17	REQUESTINGS
18	REQUESTS
19	SIGNAL
20	SIGNALS
21	TEST
22	TESTED
23	TESTEDS
24	TESTING
25	TESTINGS
26	TESTS
27	((((FUNCTIONAL SAME BOARD) SAME (PATH OR SIGNAL)) AND (((REQUESTING OR REQUESTED OR REQUEST) SAME (LATCHED OR LATCHING OR LATCH) SAME BOARD) SAME (TESTED OR TESTING OR TEST)) SAME (PATH OR SIGNAL)))

	Total	US-PGPUB	USPAT	USOCR	EPO	JPO	Derwent	IBM TDB
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2	283485							
3	784954							
4	280							
5	436900							
6	139081							
7	2							
8	131277							
9	178980							
10	111							
11	1936433							
12	509474							
13	409137							
14	190379							
15	0							
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17	2							
18	188460							
19	3842475							
20	2009590							
21	1391498							
22	656079							
23	1							
24	663870							
25	1231							
26	563987							
27	8							

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1			US 20020154321 A1	20021024	18	Multiple-level printhead using embedded high speed serial data and control link with on-board exposure clock generation	358/1.8
2	X		US 4282583 A	19810804	44	Microprogrammable processor control printer system	358/1.5
3	X		US 4179732 A	19791218	44	Microprogrammable processor control printer system	358/1.16
4	X		NN9407267	19940701	NA	Method for Adding an External Real Time Clock to a System Having an Internal Real Time Clock	
5	X		NB9406321	19940601	NA	JTAG Invalid State Off Chip Drive Protect Mechanism	
6	X		NB9306145	19930601		Open-Short Circuit Self Test	
7	X		NN8904367	19890401		Large System Diagnostics With Minimal Hardware	
8	X		NB80123098	19801201		Tester for Signature Analysis Verification. December 1980.	

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1			Pham, Hieu T. et al.	X							US 20020154321	
2	711/156		Khan; Sultan W. et al.								US 4282583	
3			Khan; Sultan W. et al.								US 4179732	
4												
5												
6												
7												
8												

	Search Terms
1	BOARD
2	BOARDS
3	EXCHANGE
4	EXCHANGED
5	EXCHANGEDS
6	EXCHANGES
7	EXCHANGING
8	EXCHANGINGS
9	LATCH
10	LATCHED
11	LATCHEDS
12	LATCHES
13	LATCHING
14	LATCHINGS
15	SIGNAL
16	SIGNALS
17	TEST
18	TESTED
19	TESTEDS
20	TESTER
21	TESTERS
22	TESTING
23	TESTINGS
24	TESTS
25	((((EXCHANGING OR EXCHANGED OR EXCHANGE) SAME (LATCHED OR LATCHING OR LATCH) SAME BOARD SAME SIGNAL) AND (TESTER OR TESTED OR TESTING OR TEST))

	Total	US-PGPUB	USPAT	USOCR	EPO	JPO	Derwent	IBM TDB
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2	283485							
3	791232							
4	164482							
5	0							
6	63874							
7	141057							
8	11							
9	436900							
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11	2							
12	131277							
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14	111							
15	3842475							
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17	1391498							
18	656079							
19	1							
20	110100							
21	10724							
22	663870							
23	1231							
24	563987							
25	29							

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1			US 20040225824 A1	20041111	33	Network of computing devices including a repeater for distributed arbitration digital data buses	710/314
2	X		US 20030120851 A1	20030626	31	Repeater for distributed arbitration digital data buses	710/306
3	X		US 6154799 A	20001128	31	Repeater-switch for distributed arbitration digital data buses	710/107
4	X		US 6033235 A	20000307	22	Socket apparatus particularly adapted for BGA type semiconductor devices	439/71
5	X		US 5812818 A	19980922	51	Apparatus and method for translating facsimile text transmission	703/23
6	X		US 5758109 A	19980526	31	Repeater/switch for distributed arbitration digital data buses	710/309
7	X		US 5684966 A	19971104	31	Method for operating a repeater for distributed arbitration digital data buses	710/305
8	X		US 5621899 A	19970415	32	Switch for distributed arbitration digital data buses	710/119
9	X		US 5594908 A	19970114	86	Computer system having a serial keyboard, a serial display, and a dynamic memory with memory refresh	710/67
10	X		US 5537565 A	19960716	83	Dynamic memory system having memory refresh	711/105

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1			Gafford, Thomas Austin et al.	X							US 20040225824	
2			Gafford, Thomas Austin et al.								US 20030120851	
3	710/119		Gafford; Thomas Austin et al.								US 6154799	
4	439/331		Ikeya; Kiyokazu								US 6033235	
5	358/402; 358/407; 382/187; 382/188; 382/228; 382/229; 704/2; 704/3; 704/8; 715/531		Adler; Richard et al.								US 5812818	
6			Gafford; Thomas Austin et al.								US 5758109	
7	710/2; 710/309; 710/316; 711/147		Gafford; Thomas Austin et al.								US 5684966	
8			Gafford; Thomas A. et al.								US 5621899	
9			Hyatt; Gilbert P.								US 5594908	
10	711/109		Hyatt; Gilbert P.								US 5537565	



	U	1	Document ID	Issue Date	Pages	Title	Current OR
11	X		US 5398276 A	19950314	58	Cellular-system signal-strength analyzer	379/21
12	X		US 4942516 A	19900717	80	Single chip integrated circuit computer architecture	712/32
13	X		US 4896260 A	19900123	83	Data processor having integrated circuit memory refresh	711/106
14	X		US 4839640 A	19890613	40	Access control system having centralized/distributed control	340/5.33
15	X		US 4829419 A	19890509	65	Microcomputer control of machines	700/83
16	X		US 4825364 A	19890425	41	Monolithic data processor with memory refresh	711/106
17	X		US 4686622 A	19870811	77	Computer system architecture using serial communication	712/32
18	X		US 4531182 A	19850723	45	Machine control system operating from remote commands	700/2
19	X		US 4445776 A	19840501	75	High resistation photomask machine and computerized numerical control system	355/78
20	X		US 4396976 A	19830802	48	System for interfacing a computer to a machine	700/1
21	X		US 4371923 A	19830201	72	Computer system architecture	712/32
22	X		US 4370720 A	19830125	72	Coordinate rotation for numerical control system	700/194
23	X		US 4364110 A	19821214	55	Computerized machine control system	700/180

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
<b>11</b>	356/4.01;		Lemke; Kurt T. et al.								US 5398276	
	379/1.01;											
	455/115.3;											
	455/161.3;											
	455/226.2;											
	455/423;											
<b>12</b>	455/67.13											
	968/900;		Hyatt; Gilbert P.								US 4942516	
<b>13</b>	968/DIG.1		Hyatt; Gilbert P.								US 4896260	
	968/900;											
<b>14</b>	968/DIG.1		Ozer; Richard et al.								US 4839640	
	235/382;											
	340/3.51;											
	340/5.28;											
<b>15</b>	340/5.5		Hyatt; Gilbert P.								US 4829419	
<b>16</b>	365/222;		Hyatt; Gilbert P.								US 4825364	
	712/32;											
	968/900;											
	968/DIG.1											
<b>17</b>	345/156;		Hyatt; Gilbert P.								US 4686622	
	345/475;											
	710/73											
<b>18</b>	700/180		Hyatt; Gilbert P.								US 4531182	
<b>19</b>	355/54;		Hyatt; Gilbert P.								US 4445776	
	396/548											
<b>20</b>			Hyatt; Gilbert P.								US 4396976	
<b>21</b>			Hyatt; Gilbert P.								US 4371923	
<b>22</b>			Hyatt; Gilbert P.								US 4370720	
<b>23</b>	700/184		Hyatt; Gilbert P.								US 4364110	

	U	1	Document ID	Issue Date	Pages	Title	Current OR
24	X		US 4120583 A	19781017	75	High registration photomask method and apparatus	355/86
25	X		US 3173542 A	19650316	13	Document handling device	209/534
26	X		US 2974199 A	19610307		Telephone paystation circuit with coin-accept control	379/146
27	X		US 2349304 A	19440523		Selective signaling system and apparatus	178/23R
28	X		US 2211116 A	19400813		Telephone system	379/239
29	X		US 1802643 A	19310428		Electrical meter-reading system	340/870.02

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
24	355/132;		Hyatt; Gilbert P.								US 4120583	
	355/95;											
	700/192;											
	716/19											
25	312/212; 902/7		SIMJIAN LUTHER G								US 3173542	
26	194/202		BELLAMY JOHN I									
27	340/825.26		POTT'S LOUIS M									
28			ANTHONY HENSLESLER BERNARD									
29	200/8R;		FLOYD GEORGE G									
	340/286.01;											
	340/870.19;											
	377/87;											
	379/106.03; 73/198											

	Search Terms
1	BOARD
2	BOARDS
3	CONTROLLER
4	CONTROLLERS
5	LATCH
6	LATCHED
7	LATCHES
8	LATCHES
9	LATCHING
10	LATCHINGS
11	PATH
12	PATHS
13	RAID
14	RAIDS
15	REDUNDANT
16	REDUNDANTS
17	SIGNAL
18	SIGNALS
19	STORAGE
20	STORAGES
21	TEST
22	TESTED
23	TESTEDS
24	TESTER
25	TESTERS
26	TESTING
27	TESTINGS
28	TESTS
29	((RAID OR (REDUNDANT SAME CONTROLLER SAME STORAGE)) AND (((LATCHED OR LATCHING OR LATCH) SAME BOARD) SAME (TESTER OR TESTED OR TESTING OR TEST)) SAME (PATH OR SIGNAL)))

	Total	US-PGPUB	USPAT	USOCR	EPO	JPO	Derwent	IBM TDB
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19	2239407							
20	6509							
21	1391498							
22	656079							
23	1							
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25	10724							
26	663870							
27	1231							
28	563987							
29	2							

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1			US 5034686 A	19910723	36	Weapon interface system evaluation apparatus and method	324/537
2	X		US 2167675 A	19390801	6	Motor vehicle door lock	180/286

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	102/206; 89/1.59; 89/1.819		Aspelin; David J.	X							US 5034686	
2	180/281; 292/144		JOSEPH PALMER								US 2167675	



	Search Terms
1	COTTRELL-ANDREW
2	COTTRELL-ANDREWS
3	HOLT-KEITH
4	HOLT-KEITHS
5	STOVER-JEREMIES
6	STOVER-JEREMY
7	STOVER-JEREMYS
8	((COTTRELL-ANDREW.IN.) OR (STOVER-JEREMY.IN.)) OR (HOLT-KEITH.IN.))

	Total	US-PGPUB	USPAT	USOCR	EPO	JPO	Derwent	IBM TDB
1	0							
2	0							
3	5							
4	0							
5	0							
6	0							
7	0							
8	5							

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	X		US 20050030891 A1	20050210	12	Method and apparatus to select an adaptation technique in a wireless network	370/229
2	X		US 20040096017 A1	20040520		Pre-corrupting reference signals with inter-symbol interference	375/343
3	X		US 6823472 B1	20041123	10	Shared resource manager for multiprocessor computer system	714/10
4	X		JP 2003015824 A	20030117	9	HIGH BANDWIDTH STORAGE DEVICE ARCHITECTURE	
5	X		EP 1271334 A2	20030102	9	High bandwidth storage device architecture	

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	370/310		Stephens, Adrian P. et al.	X							US 20050030891	
2	375/340		Holt, Keith et al.								US 20040096017	
3	710/56		Dekoning; Rodney A. et al.								US 6823472	
4			HENRY, RUSSEL J et al.								JP 2003015824 A	
5			HENRY, RUSSEL J et al.								EP 1271334 A2	



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